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# **Reports**

# Efficient and stable large-area perovskite solar cells with inorganic charge extraction layers

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The recent stunning rise in power conversion efficiencies (PCEs) of perovskite solar cells (PSCs) has triggered worldwide intense research. However, high PCE values have often been reached with poor stability at an illuminated area of typically less than  $0.1 \text{ cm}^2$ . We used heavily doped inorganic charge extraction layers in planar PSCs to achieve very rapid carrier extraction even with 10-20 nm thick layers avoiding pinholes and eliminating local structural defects over large areas. This robust inorganic nature allowed for the fabrication of PSCs with an aperture area  $>1$  cm<sup>2</sup> showing a power conversion efficiency (*PCE*) >15% certified by an accredited photovoltaic calibration laboratory. Hysteresis in the currentvoltage characteristics was eliminated; the PSCs were stable: >90% of the initial PCE remained after 1000 hours light soaking.

Organic–inorganic metal halide perovskite solar cells (PSCs) have attracted large attention due to the meteoric rise in their solar to electric power conversion efficiency (*PCE*) over the last few years (*1*). In particular, methylammoniun ( $CH_3NH_3PbI_3$ , denoted as  $MAPbI_3$ ) and formamidinium lead iodide (CH(NH2)2PbI3) emerged as a highly attractive solar light harvesting materials because of their intense broad–band absorption, high charge carrier mobility, low–cost precursor materials and simple solution processing (*2*, *3*). Their ambipolar semiconducting characteristics further enable variable device architectures, ranging from mesoscopic to planar structures with n–i–p or p–i–n layouts (4). Mesoporous TiO<sub>2</sub>-based PSCs have reached the highest performance (*5*–*7*); their certified *PCE* attaining presently 20.1% (*8*). However, there is growing interest in inverted (p–i–n) planar device architectures typically employing a MAPbI<sub>3</sub>–PCBM ([6,6]–phenyl–C<sub>61</sub>– butyric acid methyl ester) bilayer junction, because of their simple fabrication and relatively small hysteresis (*9*–*11*). A key question that remains open to date is the true power conversion efficiency and stability of planar PSCs as none of the devices has been certified so far and their stability

remains largely unexplored. Only hole–conductor–free mesoscopic PSCs using carbon as a back contact have shown so far promising stability under long term light soaking and long term heat exposure, but their certified *PCE* remains relatively low at 12.8% (*12*, *13*).

Regardless of their architectures, all high efficiency PSCs so far employed small areas, their device size being often  $< 0.1$  cm<sup>2</sup> (Table S1) (*14*). As such a small device size is prone to induce measurement errors, an obligatory minimum cell area of >1 cm2 is required for certified *PCE*s to be recorded in the standard "Solar Cell Efficiency Tables" edited by public test centers, such as National Renewable Energy Laboratory (NREL) in the US and the National Institute of Advanced Industrial Science and Technology (AIST) in Japan (*15*). It has been recommended that the record efficiencies should be recorded with cell size of ideally 1 to 2 cm2 or larger to allow comparison of competing technologies (*16*–*19*). Although a few works reported attempts of fab-

ricating centimeter-scale PSCs, for example by using vacuum evaporation system (*20*) or modified two-step approach  $(21)$  to produce large area MAPbI<sub>3</sub> films, the *PCEs* obtained for these devices reached only 10.9%~12.6%. Apart from the small device areas, the widely recognized hysteresis and stability issues of PSCs have raised doubts on the reliability of previously claimed high efficiencies (*22*, *23*).

The poor reproducibility and lack of uniformity of PSCs render it challenging to obtain high efficiencies with large devices. It is difficult to control the formation of cracks and pinholes in the selective carrier extraction layers over large areas. As small size PSCs typically show a wide spread in their *PCE*s, previous work has focused on improving the uniformity of perovskite layer by varying its deposition methods (*3*, *6*, *10*). However, fewer studies have aimed at identifying selective extraction layers for photogenerated charge carriers placed over the current collector to prevent their recombination at its surface (*24*–*26*); event though such selective contacts have turned out to be equally important to developing efficient solar light harvesters (*27*). The dilemma with optimizing of such charge carrier extraction layers in solar cells is that the film should be thin to

mimimize resistive losses while at the same time it should cover the whole collector area in a contiguous and uniform manner. Meeting these requirement becomes increasingly difficult as the device area increases.

Here we present a strategy that addresses simultaneously the scale up and stability issues facing current PSC embodiments. We develop heavily p–doped  $(p^*)$  Ni<sub>x</sub>Mg<sub>1-x</sub>O and n–doped  $(n^+)$  TiO<sub>x</sub> contacts to extract selectively photogenerated charge carriers from an inverted planar  $MAPbI<sub>3</sub>$ PCBM film architecture. We implement the  $p^+$  and  $n^+$  doping by substituting  $Ni(Mg)^{2+}$  ions and  $Ti^{4+}$  ions on the  $Ni_xMg_{1-x}O$  lattice and TiO<sub>x</sub> matrix by Li<sup>+</sup> and Nb<sup>5+</sup> ions, respectively. The resulting dramatic increase in the electrical conductivity enables 10-20 nm thick oxide layers to be used for selective extraction of one type of charge carriers while improving their electronic blocking effect for the other type by reducing the density of pinholes and cracks over large areas. Accordingly, the series resistance  $(R_s)$  of the oxides decreased and the shunt resistance  $(R_{sh})$  greatly increased with respect to the undoped ones, allowing excellent fill factor (*FF*) with values exceeding 0.8 and hysteresis–free behavior to be achieved. With this strategy, we successfully fabricated large size  $(>1$  cm<sup>2</sup>) PSCs with an efficiency of up to 16.2%. A *PCE* of 15% was certified by a public test center (Calibration, Standards and Measurement Team at the Research Center for Photovoltaics, National Institute of Advanced Industrial Science and Technology, AIST, Japan). This is listed as the first official efficiency entry for PSCs in the most recent edition of the "Solar Cell Efficiency Tables" (28). Moreover, the devices based on these stable  $p^+$  and  $n^+$ doped inorganic metal oxides charge extraction layers show high stability rendering them attractive for future practical deployment of PSCs.

We fabricated PSCs with an inverted planar device architecture (Fig. 1A); a cross sectional scanning electron microscopy (SEM) image of the device is shown in Fig. 1B. We first deposited the NiO based hole extraction layer onto fluorinedoped tin oxide (FTO) glass using spray pyrolysis. The precursor solution was composed of nickel (II) acetylacetonate alone or together with doping cations  $(Mg^{2+})$  from magnesium acetate tetrahydrate, Li<sup>+</sup> from lithium acetate) in superdehydrated acetonitrile/ethanol mixture. The  $MAPbI<sub>3</sub>$  perovskite layer with thickness of ~300 nm was deposited by a reported method (*6*), which was followed by the deposition of a thin PCBM layer (80 nm) via spin–coating its chlorobenzol solution (20 mg ml<sup>-1</sup>) at 1000 rpm for 30 s. An n-type  $TiO_x$  based electron extraction layer with and without  $Nb^{5+}$ doping was further deposited on the PCBM by spin–coating a diluted methanol solution of titanium isopropoxide (or mixed with niobium ethoxide), followed by controlled hydrolysis and condensation (*14*). Finally, the device was completed by thermal evaporation of a 100 nm thick Ag cathode. The band alignments of relevant functional layers are shown in Fig. 1C, based on the energy levels determined by ultraviolet (UV) photoelectron spectroscopy (UPS) and ultraviolet–visible (UV–Vis) absorption spectroscopy measurements (fig. S1) (*14*). The uniformity of the perovskite and PCBM layers was examined by cross sectional SEM observation (fig. S2) ( $14$ ). The full XPS spectra of the NiO and TiO<sub>x</sub> based charge carrier extraction layers are shown in fig. S3 (*14*), revealing the designated compositions for the target materials. The close–up observation on the morphology of the charge carrier extraction layers are depicted in fig. S4 (*14*), while their pin–hole densities were examined by electrical measurement as discussed below.

The stoichiometric form of NiO is a wide band gap semiconductor with a very low intrinsic conductivity of 10−13 S  $cm^{-1}$  (29). Self-doping by introducing Ni<sup>3+</sup> acceptors into the NiO crystal lattice renders the crystals more conductive, depending on the film deposition techniques and conditions (*11*, *30*–*32*). The room temperature specific conductivity of our NiO films from Hall effect measurements was 1.66 × 10−4 S cm–1 . This value is much lower than the typically used p–type contact layer of PEDOT: PSS that shows a conductivity of 1 to 1000 S  $cm^{-1}$  (33). The low conductivity of NiO will lead to a high *R*<sup>s</sup> resulting in a low *FF* of the solar cells (*34*). Substitutional doping by  $Li<sup>+</sup>$  is an effective way to increase the p-conductivity of NiO (35). Values of heavily (p<sup>+</sup>)-doped NiO films can reach 1 to 10 S  $cm<sup>-1</sup>$  at room temperature under optimal conditions (36). For our Li<sup>+</sup> doped  $Ni_xMg_{1-x}O$ films the conductivity is  $2.32 \times 10^{-3}$  S cm<sup>-1</sup>, ~12 times greater than that of the undoped reference.

A  $Mg^{2+}$  content of 15 mol% was alloyed in the Li<sup>+</sup> doped nickel oxide film, to compensate the undesirable positive shift of its valence band  $(E_{VB})$  caused by Li<sup>+</sup> incorporation into the lattice (fig. S1)  $(14, 35, 37, 38)$ . As the Li<sup>+</sup> content was adjusted to 5 mol%, the doped oxide has the formula of  $Li_{0.05}Mg_{0.15}$  Ni<sub>0.8</sub>O if one assumes that the molar ratio of the three different cations in the spray pyrolyses solution is maintained in the mixed oxide. This co–doping strategy is feasible because the mismatch of the ionic radii of Li+  $(0.76\text{\AA})$ , Mg<sup>2+</sup>  $(0.71\text{\AA})$  and Ni<sup>2+</sup>  $(0.69\text{\AA})$  is quite small, conferring good lattice stability to the  $Li_xMg_vNi_{1-x-y}O$  ternary oxides. We compared conductivity of NiO and  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$ films by using contact–current mode of a scanning probe microscope (SPM) and show the results in Fig. 2, A and B. At a bias potential of 1.0 V, the electric current increased by a factor of  $~10$  (from  $~0.3$  nA to  $~3$  nA) upon replacing undoped NiO by a  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$  film. The XPS spectra in fig. S6 (*14*) reveal that the doping increased the relative content of  $Ni^{3+}$  acceptors in the  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$  sample. These findings are consistent with reports on  $Li<sup>+</sup>$  doped NiO films in (*32*) and explains the increase in carrier concentration from 2.66  $\times$   $10^{17}$   $\rm cm^{-3}$  of the undoped NiO film to  $6.46$   $\times$   $10^{18}$  $cm^{-3}$  for  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$  film that we derived from Hall effect measurements.

The electron specific n–type  $TiO_x$  contact used commonly for organic PV is normally fabricated by hydrolysis and condensation of titanium alkoxides at temperature below 150°C  $(39, 40)$ , where the crystallization of TiO<sub>2</sub> is slow. In order to

prevent heat-induced degradation of the perovskite layer and the adjacent interfaces, we kept the annealing temperature of TiO<sub>x</sub> films below 70°C. While such TiO<sub>x</sub> films have been used extensively in organic PVs, details on their structure and mechanism of electric conduction have so far not been elucidated (*41*). One commonly recognized problem is that the amorphous nature of  $TiO_x$  leads to extremely low specific conductivities that are in the range of 10<sup>-8</sup>–10<sup>-6</sup> S  $cm^{-1}$  (42). Nb<sup>5+</sup> doping has proved to be effective for enhancing the conductivity of crystalline anatase  $TiO<sub>2</sub>$  films to  $\sim 10<sup>4</sup>$ S cm–1 , enabling its use as a transparent conducting oxide similar to conventional indium tin oxide (*43*). By analogy, this dopant is expected to improve also the conductivity of the amorphous  $\text{TiO}_x$  via substitution of  $\text{Ti}^{4+}$  by Nb<sup>5+</sup> which is expected to create donor centers. From the current-voltage (*I*–*V*) curves obtained by SPM measurement shown in Fig. 2B, the conductivity of  $TiO<sub>x</sub>$  film was estimated to increase from about  $10^{-6}$  S cm<sup>-1</sup> to  $10^{-5}$  S cm<sup>-1</sup> upon adding 5 mol%  $Nb<sup>5+</sup>$  to the precursor solution. By resolving the XPS spectra in fig. S6  $(14)$ , the relative content of  $Ti^{3+}$  in comparison to  $Ti<sup>4+</sup>$ , i.e., the donor species in the TiO<sub>x</sub> film responsible for its n–type conductivity, has increased via  $Nb<sup>5+</sup>$  doping.

We derived the optimal thickness of the NiO- and  $TiO<sub>x</sub>$ based charge-extraction layers from the electrical measurements shown in fig. S7 (*14*). A complete layer with no pinholes of NiO and TiO<sub>x</sub> requires at least thickness of 20 nm and 10 nm, respectively, regardless of the presence of dopants. These minimum thicknesses should depend on the underlayers' (FTO for NiO, or PCBM for  $TiO_x$ ) surface chemistry and morphology, as well as the fabrication methods used for the NiO and TiO<sub>x</sub> films. We compared small solar cells with size of 0.09 cm2 and varied the NiO layer thickness from 10, 20 to 40 nm, keeping that of the  $Ti_{0.95}Nb_{0.05}O_x$ fixed at 10 nm. Conversely, we fixed the  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$ layer thickness at 20 nm and varied that of the  $TiO_x$  films from 5 to 30 nm. For each condition, 20 cells were made and measured to establish any underlying trends. As shown in fig. S8 (*14*), although high performance can be occasionally achieved from devices with very thin charge carrier extraction layers (~10 nm NiO or ~5 nm TiO<sub>x</sub>), most devices showed lower *PCE*s because of lower *FF* and open*–*circuit voltage  $(V_{oc})$  (fig. S9) (14), which can be attributed to the presence of substantial levels of pinholes. The reproducibility of device performance was greatly enhanced as the thickness of charge extraction layers increased, while the optimal performance was attained with 20 nm NiO and 10 nm  $TiO_x$  film, in agreement with the electrical measurement. Further increasing the film thickness of the two charge extraction layers can lead to a large efficiency decline caused by increased internal resistance, larger optical loss, or both (fig. S10) (*14*). Thus, we fixed the thickness of NiO and  $TiO<sub>x</sub>$  at 20 nm and 10 nm, respectively for the following studies of doping effect on device performance.

Figure 3A shows the effect of doping the NiO and  $TiO<sub>x</sub>$ charge extraction layers on the photocurrent density*–*

voltage (*J–V*) curves of PSCs measured under simulated AM 1.5 sunlight with forward scanning direction. The short circuit current  $(J_{\rm sc})$ ,  $V_{\rm oc}$ ,  $FF$ , and PCE data are listed in Table S2 ( $14$ ). Both doping of NiO and TiO<sub>x</sub> reduced  $R_s$  and improved the *FF*, and to a lesser extent  $J_{\rm sc}$  and  $V_{\rm oc}$ . The TiO<sub>x</sub> electron extraction layers mainly affect the shape of *J–V* curves in the forward bias range from 0.7 to 1.0 V, where a Schottkybarrier type contact between PCBM and Ag strongly restricted efficient electron collection (fig. S9A) (*14*, *44*). The  $Nb<sup>5+</sup>$  doping of TiO<sub>x</sub> reduced the interfacial electron transfer resistance and facilitated electron transport, increasing the photocurrent especially in the 0.7 to 1.0 V forward bias region. The NiMg(Li)O-based hole extraction layer promoted ohmic contact formation at the FTO–MAPbI<sub>3</sub> interface by decreasing the barrier height through the staircase energy level alignment shown in Fig. 1C. P+–doping increased the electrical conductivity by decreasing the charge transport resistance and hence enhancing hole extraction.

Doping of both NiO and  $TiO_x$  improves the cell performance by increasing the values of  $FF$  and  $V_{\text{oc}}$  to 0.827 and 1.083 V, respectively, leading to *PCE* of 18.3% for this planar PSC with MAPbI<sub>3</sub>. In comparison to PEDOT:PSS based PSCs, the  $V_{oc}$  increased by  $\sim 100$  mV, indicating that with  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$ , the holes can be extracted at a higher energy level (*10*). Furthermore, the *FF* of 0.83 is one of the highest values for reported PSCs (*8*, *9*, *23*), demonstrating the key role of the charge extraction layers in minimizing resistive losses and improving the photovoltaic performance.

To gain further insight into the reasons for the performance enhancement by the doping, we characterized the charge carrier extraction, transportation and recombination by nanosecond time–resolved photoluminescence (PL) decay using a picosecond laser flash as excitation source and by measuring transient photocurrent/photovoltage decays on the microsecond scale. The charge extraction involved in our cells include the electron transfer from the  $MAPbI<sub>3</sub>$  absorber layer to  $PCBM/TiO<sub>x</sub>$  and hole transfer to NiO as well as the carrier transport in the TiOx and NiMg(Li)O layers. The perovskite/PCBM interface has been demonstrated to be very efficient for electron extraction (*9*–*11*). Doping of the  $TiO<sub>x</sub>$  extraction layer is unlikely to have a direct impact on the electron injection rate because of it physical separation from the  $MAPbI<sub>3</sub>$  by 80 nm-thick PCBM layer. Nevertheless, it greatly accelerates the electron extraction by decreasing the electron transport time as shown in Fig. 3C. Figure 3B shows the PL decays of the MAPbI<sub>3</sub> films on different substrates, including a glass slide, and NiO and  $Li_{0.05}Mg_{0.15}Ni_{0.8}O$ deposited on FTO glass. The MAPbI<sub>3</sub> itself showed a long PL lifetime of >100 ns, indicating slow carrier recombination in the perovskite layer (*10*). When contacted with the p*–*type hole extraction layers, the PL lifetimes were shortened to a similar degree for both doped and undoped NiO–MAPbI<sub>3</sub> contacts. Thus, doping has a negligible influence on the hole injection.

We derived the charge transport and recombination time constants ( $\tau_t$  and  $\tau_r$ ) from the transient photocurrent and photovoltage decays measured at short and open circuit, respectively (Fig. 3C). The  $\tau_t$  decreased by ~5 fold from 4.41 μs for undoped cell to 0.88 μs for doped cell, confirming the much faster charge transport through the doped charge carrier extraction layers compared to the undpoed ones. However, the  $\tau_r$  of a doped cell is substantially longer than that of an undoped one (84.8 vs. 50.5 μs, Fig. 3D), which we attribute to slower interfacial charge recombination since the very rapid carrier collection prevents charge accumulation at the interface of the pervovskite with the charge extraction layer. The doping-induced difference in charge transport/recombination kinetics should be the main reason responsible for their performance enhancement.

The hysteresis of our cells was examined by using different scan rates and directions. By decreasing the step width from 70 mV to 5 mV, the *PCE*s determined from the forward scan slightly increased from 18.14% to 18.35%. However, the reverse scan *PCE*s decreased substantially from 22.35% at 70 mV/step (fig. S11A and table S3 (*14*)) to 18.40% at 5 mV/step. The steady power outputs measurements (fig. S11D) (*14*) indicate that the *PCE*s obtained in forward scans and at small step widths (5*–*10 mV) are near the real performance. The *V*oc and *PCE* obtained at a fast reverse scan, *i.e.*, 1.273 V and 22.35% at 70 mV/step, are largely overestimated. With the step widths of 5–10 mV, the *PCE* deviations between forward and reverse scans are very small, *i.e*., within 0.3% in absolute *PCE* values reflecting negligible hysteresis. A histogram comparing the difference in the *PCE*s obtained from scanning in the forward and reverse bias directions is shown in fig. S11C (*14*), supporting the absence of hysteresis for the optimized device architecture. In stark contrast, for undoped charge extraction layers, a pronounced hysteresis was observed even for slow scan rates (fig. S11E) (*14*), which is likely to arise from unbalanced charge accumulation at the two interfaces  $(45)$ . Thus, the  $Li<sub>0.05</sub>Mg<sub>0.15</sub>Ni<sub>0.8</sub>O$  and  $Ti<sub>0.95</sub>Nb<sub>0.05</sub>O<sub>x</sub>$  charge extraction layer create a robust low impedance interface that can mitigate the *J–V* hysteresis under a routine scanning condition.

We fabricate cells with active area  $>1$  cm<sup>2</sup> as a first step toward scale up of the photovoltaic devices. Figure 4A shows the *J–V* curve of such cell with aperture area of 1.02 cm2 . It shows excellent performance, with *J*sc, *V*oc, and *FF* reaching values of 20.21 mA cm*–*<sup>2</sup> , 1.072 V and 0.748, respectively, corresponding to a *PCE* of 16.2%. Hysteresis for these large area devices is also small (fig. S12) (*14*). The corresponding *IPCE* (Fig. 4B) shows a broad plateau with maximum value of 90.1% over practically the whole visible range. The integrated  $J_{\rm sc}$  from *IPCE* matches well with the measured value. Compared to small size cells  $(0.09 \text{ cm}^2)$ , a  $\sim 10\%$ decrease in *PCE* was observed in large size cells (1.02 cm<sup>2</sup>), which is mainly caused by the large sheet resistance of the FTO. We sent one of our best large cells to a public test center (AIST, Japan) for certification. A *PCE* of 15.0% for a 1.017

cm2 device was certified (fig. S13) (*14*).

To demonstrate the superiority of the solution processible  $Ti_{0.95}Nb_{0.05}O_x$  charge carrier extraction layer, two references, *i.e.*, Ca (4nm)/Ag, LiF (1.5nm)/Ag that were deposited by thermal evaporation, were compared with our best interfacial condition. As shown in Fig. 4C, without sealing, the  $Ti(Nb)O<sub>x</sub>$  based PSC shows the best stability since its *PCE* only decreased by ~5% of its initial value after 1 week. The Ca/Ag based PSC showed the fastest degradation, which lost its initial *PCE* by >30% after 1 day and by ~45% within 1 week. This difference is attributed to the fast oxidization of very reactive Ca, leading to dramatic loss on  $J_{\rm sc}$  and *FF*. The LiF/Ag based PSC lost 15% of its initial *PCE* within 1 week. The extremely thin LiF layer (< 2.5 nm, as required by efficient tunneling) (*46*) and the high sensitivity of LiF to moisture is likely to be responsible for the corresponding cell's inferior stability. It is possible that the stability of Ca or LiF based devices can be improved if they are thoroughly sealed. However, the requirement on sealing quality will be much more critical in comparison to the air–stable interface of Ti0.95Nb0.05Ox (*39*).

The  $Ti_{0.95}Nb_{0.05}O_x$  layer also shields the perovskite from the intrusion of humidity. It assumes a similar role in OPVs (39). We exposed bare MAPbI<sub>3</sub>, MAPbI<sub>3</sub>/PCBM and MAP $bI_3/PCBM/Ti(Nb)O_x$ , to ambient air under room light for 3 weeks. A striking difference in color degradation associated with perovskite decomposition became clearly visible (fig. S14) (*14*). Thus it appears that the hydrophobic nature of PCBM may protect the perovskite from reaction with water, while the coating of  $Ti(Nb)O<sub>x</sub>$  could further enhance the stability.

Figure 4D also shows the long*–*term stability of PSCs using the optimized inorganic charge extraction layers. The silver back contact was protected by a covering glass which was separated from the front FTO glass by a UV-activated glue used as a sealant. The cells maintained 97% of their initial *PCE* after keeping them in the dark for 1000 hours. Exposing the cells for 1000 hours at short*–*circuit condition to full sunlight from a solar simulator, resulted in a *PCE* degradation of less than 10%. This degradation is consistent with the general tendency among 10 devices, as shown in fig. S15 (*14*), indicative of their good long-term stability. During this time, an electric charge of around 72'000 C (4.49 × 1023 electrons) passed through the device. This result shows that the planar cell structure and the metal oxides extraction layers, as well as the organo*–*metal halide perovskite material, are robust enough to sustain continued current flow under light exposure for 1000 hours. A further increase in the *PCE* without sacrificing stability could be expected from varying the composition of the pervoskite, e.g., replacing part of the methylammonium cations in the  $MAPbI<sub>3</sub>$ pervovskite by formamidinium (*47*) and a small fraction of the iodide by bromide anions.

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## **SUPPLEMENTARY MATERIALS**

www.sciencemag.org/cgi/content/full/science.aad1015/DC1 Materials and Methods Figs. S1 to S15 Tables S1 to S3 References (*48–50*)

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Fig. 1. Structure and band alignments of the PSC. (A) Scheme of the cell configuration highlighting the doped charge carrier extraction layers. The right insets shows the composition of Ti(Nb)O<sub>x</sub> and the crystal structure of lithium doped  $Ni_xMg_{1-x}O$ , denoted as NiMg(Li)O. (B) A high resolution cross– sectional SEM image of a complete solar cell (the corresponding EDX analysis results are shown in fig. S5 (*14*), demonstrating the presence of the p+–doped  $Ni_xMg_{1-x}O$  and n<sup>+</sup>-doped  $TiO_x$  charge extraction<br>layers).  $( C )$  Band layers). (C) Band alignments of the solar cell. The data of  $MAPbl_3$  and PCBM are taken from (*11*).



Fig. 2. Dopant enhanced conductivity of NiO and TiO<sub>x</sub>.  $(A)$ Comparison of the conductivity mapping results for NiO (Left) and Li<sub>0.05</sub>Mg<sub>0.15</sub>Ni<sub>0.8</sub>O (Right) films. (B) Left: Comparison of the *I*-*V* curves of NiO and Li<sub>0.05</sub>Mg<sub>0.15</sub>Ni<sub>0.8</sub>O films deposited on FTO glass and Right: Comparison of the *I-V* curves for TiO<sub>x</sub> and Ti<sub>0.95</sub>Nb<sub>0.05</sub>O<sub>x</sub> films deposited on PCBM/ITO glass, obtained by SPM measurements. Thickness was 20 nm for both NiO and  $TiO<sub>x</sub>$  based films.



Fig. 3. Doping enhanced photovoltaic performance. (A) *J-V* curves of solar cells based on different combinations of charge extraction layers with standard thickness (NiO, NiMg(Li)O = 20 nm; TiO<sub>x</sub>, Ti(Nb)O<sub>x</sub> = 10 nm). (B) Normalized PL transient decay curves of perovskite and perovskite at the controlled interfaces of NiO and NiMg(Li)O, solid lines are fitted results with a double exponential decay. The time interval during which the PL decays to 1/e of the initial intensity is defined as the characteristic lifetime ( $\tau$ ) of free carriers after photoexcitation. (C) and (D) show normalized transient photocurrent and photovoltage decay curves, respectibely based on undoped and doped charge carrier extraction layers. The charge transport (τ<sub>t</sub>) and recombination time (τ<sub>r</sub>) are again defined as the time interval during which the photocurrent or photovoltage decays to 1/e of the their initial value immediately after excitation.



Fig. 4. Performance and stability of large size cells. (A) *J–V* curve of the best large cell endowed with anti*–*reflection film, (B) the corresponding *IPCE* spectrum and integrated  $J_{sc}$ , (C) the stability of the cells without sealing, based on different electron extraction layers of Ca (4 nm), LiF (1.5 nm) and  $Ti(Nb)O<sub>x</sub>$  (10 nm) between PCBM and the Ag contact, the cells were kept in a dry cabinet (< 20% humidity) in the dark and measured in ambient air,  $(D)$ stability of sealed cells kept in the dark or under simulated solar light (AM 1.5, 100 mW cm<sup>-2</sup>, using a 420 nm UV light cut-off filter, surface temperature of the cell:  $45^{\circ}$  to  $50^{\circ}$ C, bias potential = 0 V).